AMENDMENT AND RESPONSE TO NOTICE OF DRAWING INCONSISTENCY
Serial Number: 10/643,585
Filing Date: August 18, 2003
Title: LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER

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Dkt: 1376.700US1

In the Specification

On page 2 at line 19, please add the following:

Figure 11 illustrates a format for a virtual memory address.

On page 2 at line 20, please add the following:

Figure 12 illustrates a method for analyzing a VNODE field.